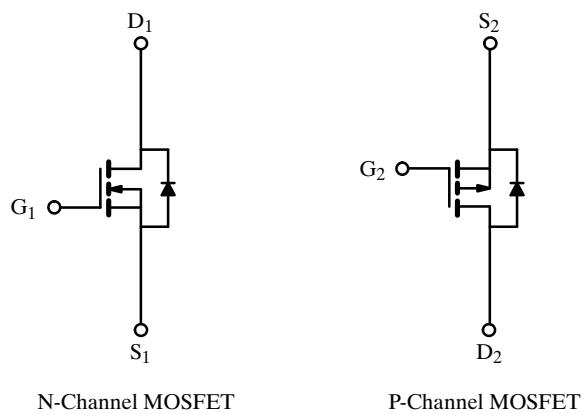
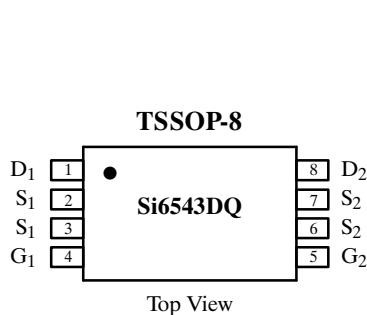


Dual Enhancement-Mode MOSFET (N- and P-Channel)

Product Summary

	V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
N-Channel	30	0.065 @ $V_{GS} = 10$ V	± 3.9
		0.095 @ $V_{GS} = 4.5$ V	± 3.1
P-Channel	-30	0.085 @ $V_{GS} = -10$ V	± 2.5
		0.19 @ $V_{GS} = -4.5$ V	± 1.8

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	30	-30	V
Gate-Source Voltage	V_{GS}	± 20	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	± 3.9	± 2.5	A
		± 3.1	± 2.1	
Pulsed Drain Current	I_{DM}	± 20	± 20	A
Continuous Source Current (Diode Conduction) ^a	I_S	1.25	-1.25	
Maximum Power Dissipation ^a	P_D	1.0	0.64	W
		0.64	0.64	
Operating Junction and Storage Temperature Range	T_J, T_{Stg}	-55 to 150		°C

Thermal Resistance Ratings

Parameter	Symbol	N- or P-Channel	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	125	°C/W

Notes

a. Surface Mounted on FR4 Board, $t \leq 10$ sec.

Subsequent updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #1814.

Specifications ($T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

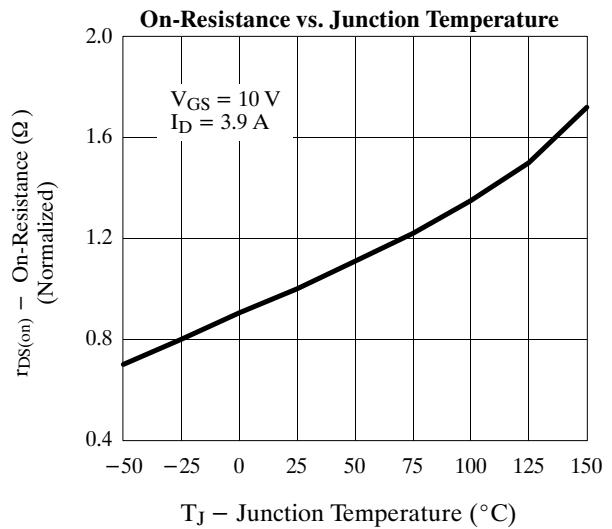
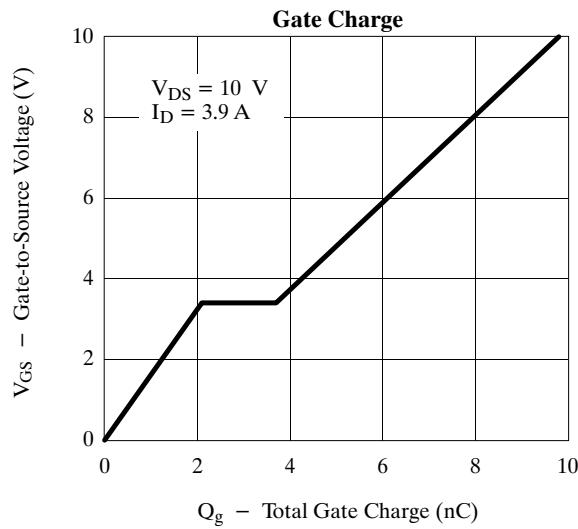
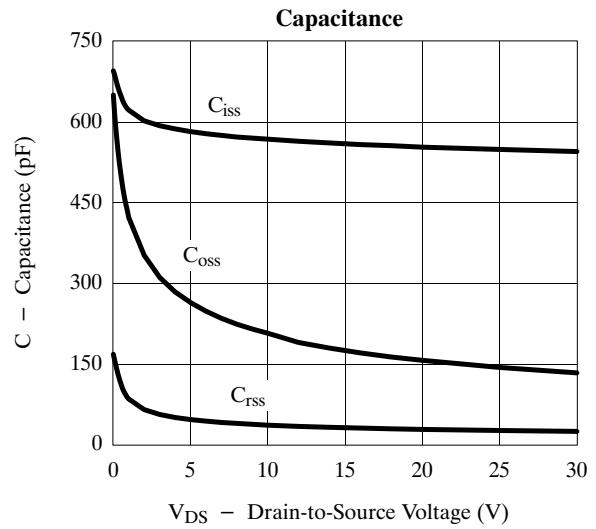
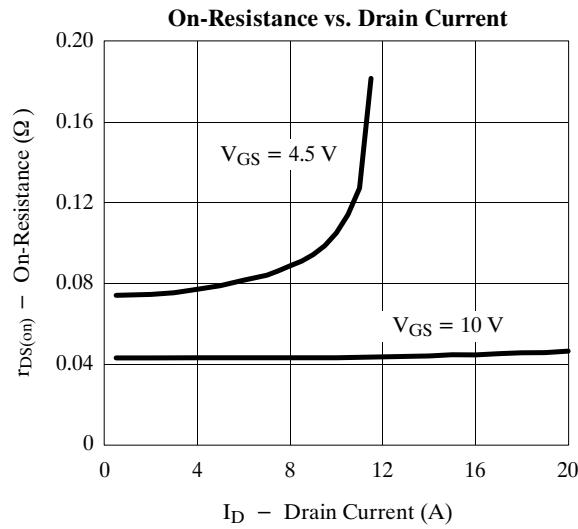
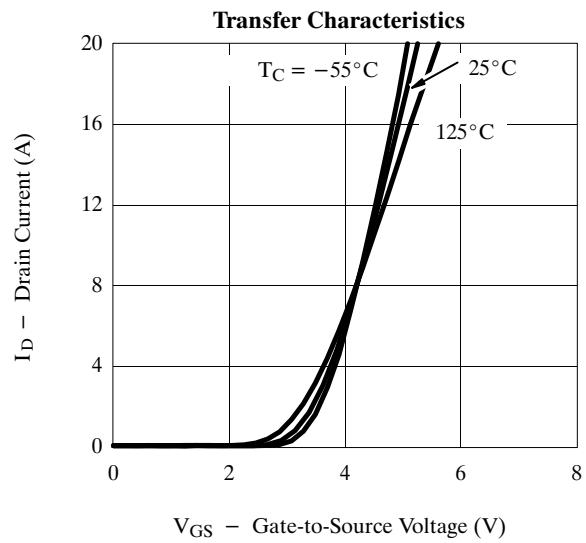
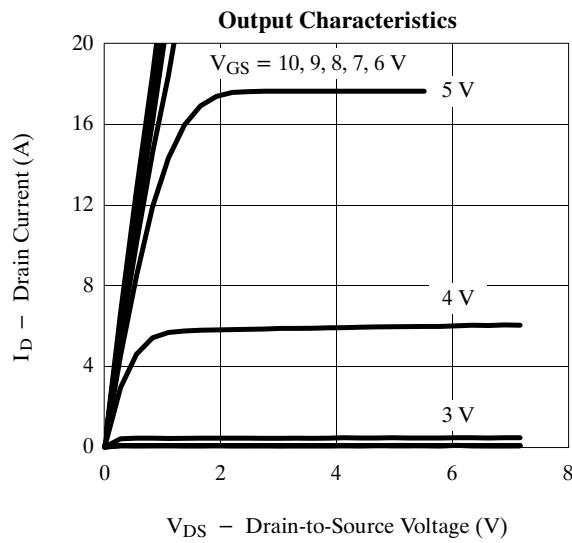
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1.0		
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-1.0		V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	N-Ch P-Ch		± 100 ± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch		1	
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch		-1	
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	N-Ch		25	μA
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	P-Ch		-25	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch P-Ch	15 -15		A
		$V_{DS} \geq -5 \text{ V}, V_{GS} = -10 \text{ V}$	N-Ch P-Ch			
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10 \text{ V}, I_D = 3.9 \text{ A}$	N-Ch		0.043	0.065
		$V_{GS} = -10 \text{ V}, I_D = 2.5 \text{ A}$	P-Ch		0.066	0.085
		$V_{GS} = 4.5 \text{ V}, I_D = 3.1 \text{ A}$	N-Ch		0.075	0.095
		$V_{GS} = -4.5 \text{ V}, I_D = 1.8 \text{ A}$	P-Ch		0.125	0.19
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 3.9 \text{ A}$	N-Ch		7	
		$V_{DS} = -15 \text{ V}, I_D = -2.5 \text{ A}$	P-Ch		5	S
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.25 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch		0.8	1.2
		$I_S = -1.25 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		0.8	-1.2
Dynamic^b						
Total Gate Charge	Q_g	N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.9 \text{ A}$ P-Channel $V_{DS} = -10 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	N-Ch P-Ch		9.8 8.7	15 15
Gate-Source Charge	Q_{gs}		N-Ch P-Ch		2.1 1.9	
Gate-Drain Charge	Q_{gd}		N-Ch P-Ch		1.6 1.3	
Turn-On Delay Time	$t_{d(on)}$		N-Ch P-Ch		9 7	15 15
Rise Time	t_r	N-Channel $V_{DD} = 10 \text{ V}, R_L = 10 \Omega$ $I_D \approx 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$ P-Channel $V_{DD} = -10 \text{ V}, R_L = 10 \Omega$ $I_D \approx -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$	N-Ch P-Ch		6 9	18 18
Turn-Off Delay Time	$t_{d(off)}$		N-Ch P-Ch		18 14	27 27
Fall Time	t_f		N-Ch P-Ch		6 8	15 15
Source-Drain Reverse Recovery Time	t_{rr}		N-Ch P-Ch		48 46	80 80

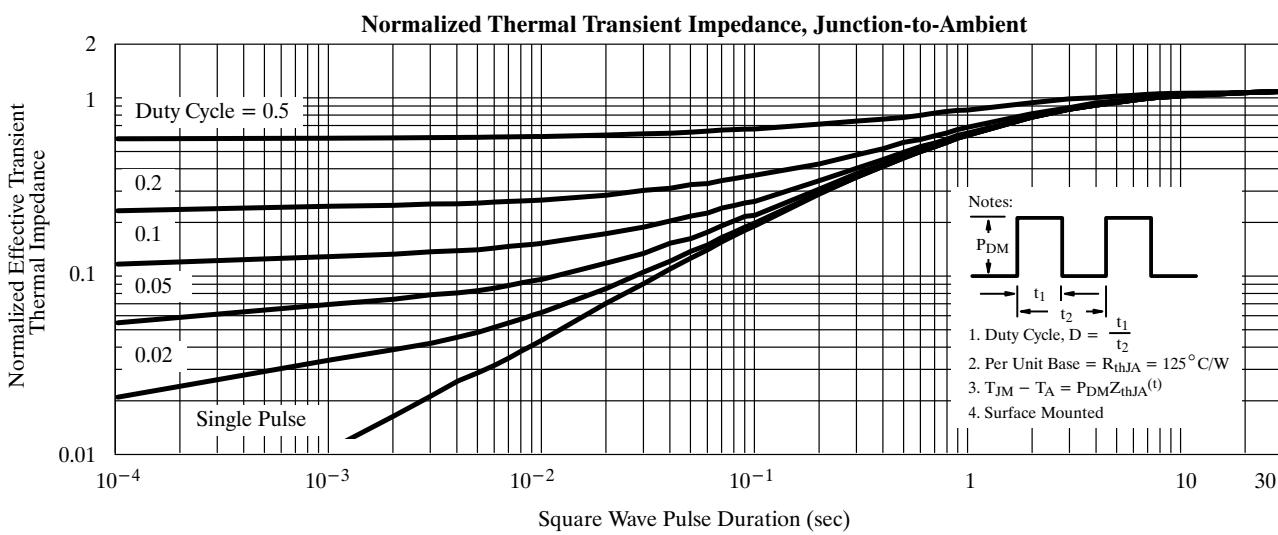
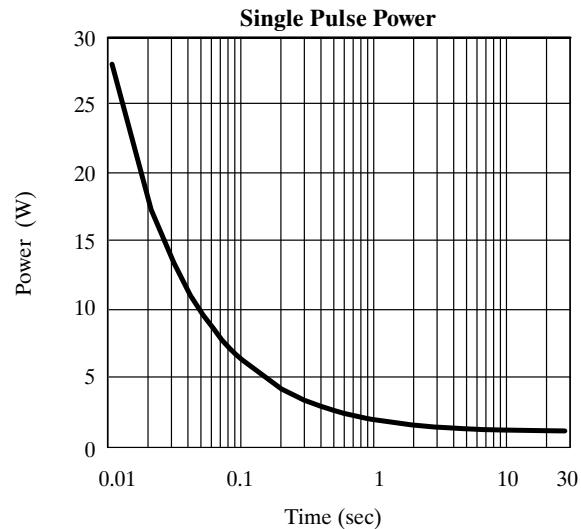
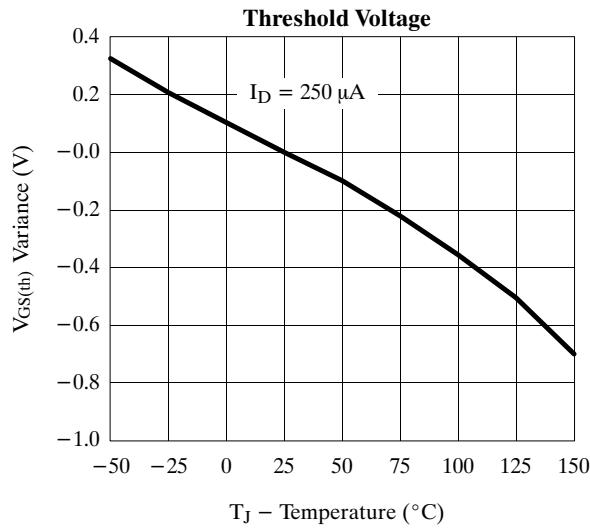
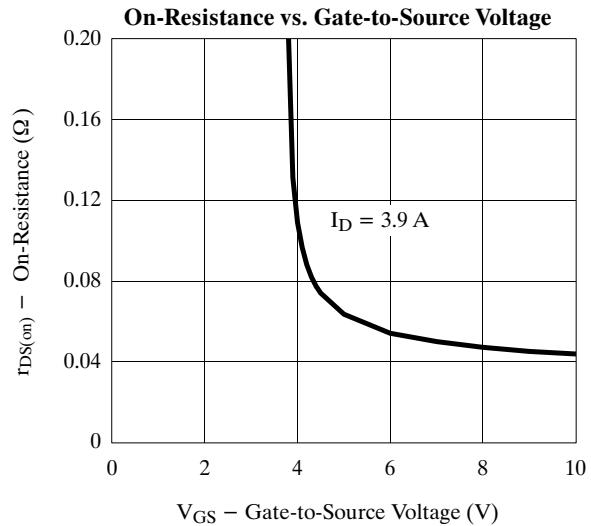
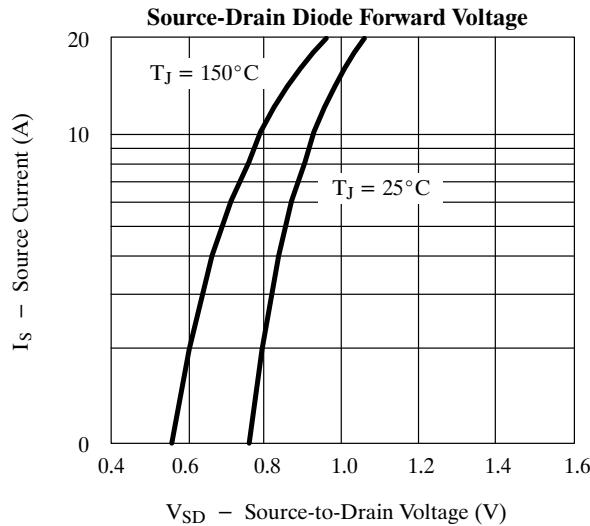
Notes

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Typical Characteristics (25°C Unless Noted)

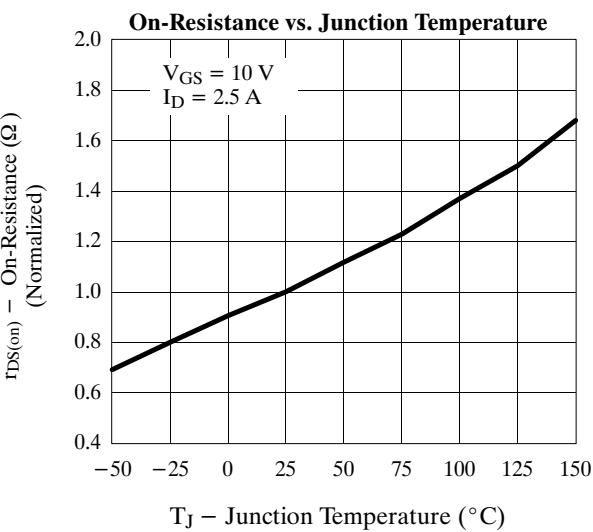
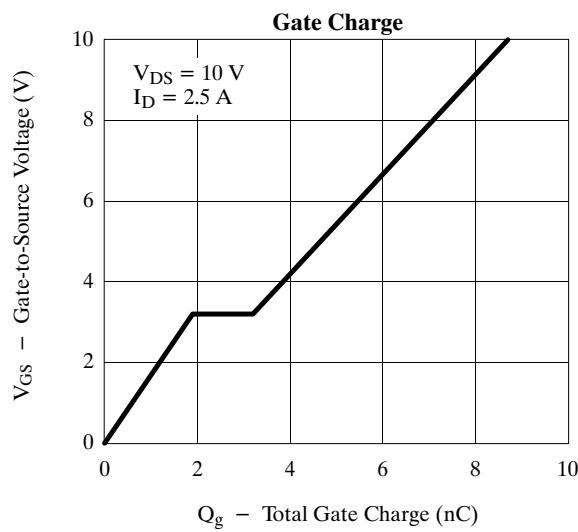
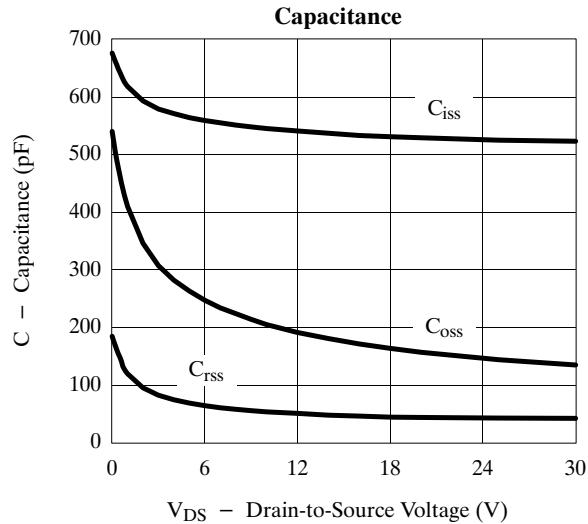
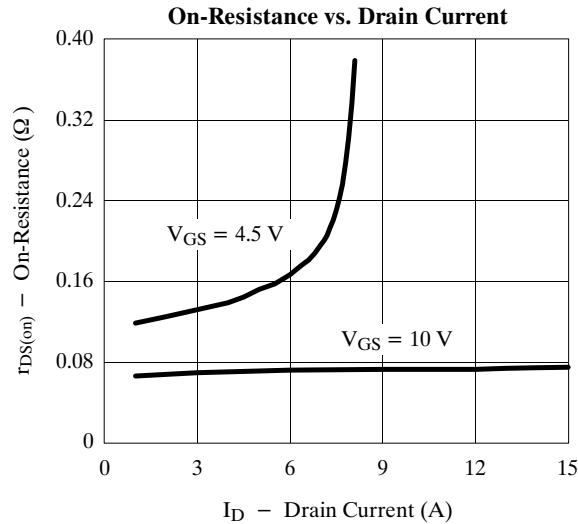
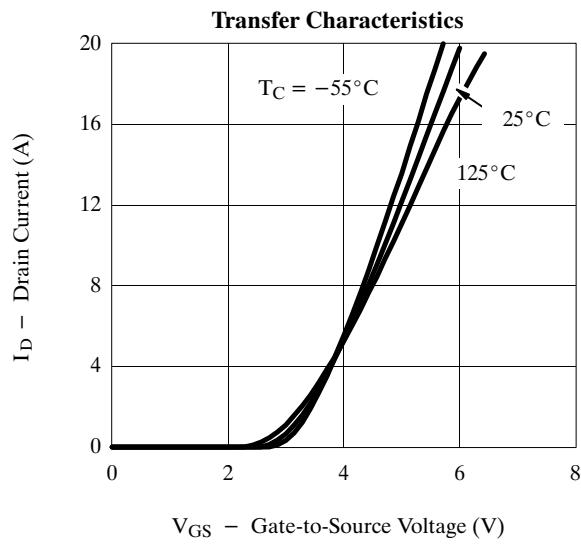
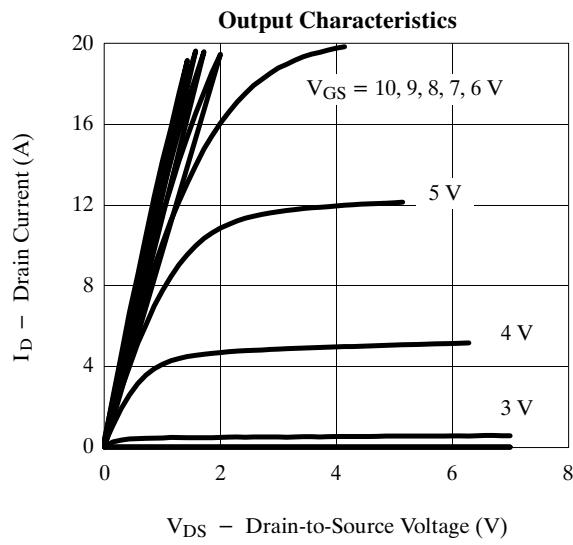
N-Channel



Si6543DQ**Typical Characteristics (25°C Unless Noted)****N-Channel**

Typical Characteristics (25°C Unless Noted)

P-Channel



Si6543DQ**Typical Characteristics (25°C Unless Noted)****P-Channel**